



TE 2112

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ken Jaramillo

Art Unit: 2112

Serial No.: 10/039,707

Examiner: Christopher E. Lee

Filed: 11-9-01

Docket: 101950.00076 – OLD #  
125273.00045 – NEW #

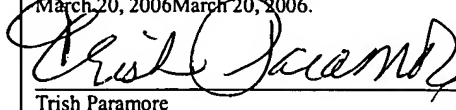
For: **METHOD FOR INCREASING PERIPHERAL COMPONENT INTERCONNECT (PCI) BUS THROUGHPUT VIA A BRIDGE FOR MEMORY READ TRANSFERS VIA DYNAMIC VARIABLE PREFETCH**

**RESPONSE TO NON COMPLIANT AMENDMENT**

March, 20, 2006

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. 1.8(A)  
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on March 20, 2006 March 20, 2006.

  
Trish Paramore

Sir:

Responsive to the Notice of Non-Compliant Amendment dated March 8, 2006, please amend the above identified application as follows:

**In the Drawings**

Enclosed please find the properly identified two pages of drawings for Figure 2 and Figure 3.